

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	0	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) and (timing adj resistor) and (timing adj capacitor)) and (clock adj signal) and ((resistor or voltage) adj divider) and 375/238-239.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/05 12:56
L7	0	@ad<"19991018" and counter and (PWM or (pulse adj width adj modulat\$3)) and resistor adj capacitor and ((resistor or voltage) adj divider) and 370/212-213.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/05 13:09
L12	730	@ad<"19991018" and counter and (PWM or (pulse adj width adj modulat\$3)) and resistor and capacitor and ((resistor or voltage) adj divider)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/05 13:02
L13	0	@ad<"19991018" and counter and (PWM or (pulse adj width adj modulat\$3)) and resistor and capacitor and ((resistor or voltage) adj divider) and 329/312-313.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/05 13:02
L14	5	@ad<"19991018" and counter and (PWM or (pulse adj width adj modulat\$3)) and resistor and capacitor and ((resistor or voltage) adj divider) and 375/238-239.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/05 13:02
L15	4	@ad<"19991018" and counter and (PWM or (pulse adj width adj modulat\$3)) and resistor and capacitor and ((resistor or voltage) adj divider) and 370/212-213.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/05 13:16
L17	4	@ad<"19991018" and counter and (PWM or (pulse adj width adj modulat\$3)) and resistor and capacitor and ((resistor or voltage) adj divider) and 332/109-112.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/05 13:17
S1	4	@ad<="19991018" and counter and (clock adj signal) and electromagnetic and 375/238.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/05 12:53
S2	470	@ad<="19991018" and counter and (PWM or (pulse adj width adj modulat\$3)) and (clock adj signal) and electromagnetic and resistor adj capacitor and 375/238.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/05 12:53
S3	235	@ad<="19991018" and counter and (PWM or (pulse adj width adj modulat\$3)) and (clock adj signal) and electromagnetic and resistor and capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/03 13:50

S4	2	@ad<="19991018" and counter and (PWM or (pulse adj width adj modulat\$3)) and (clock adj signal) and electromagnetic and resistor and capacitor and 375/238.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/03 13:57
S5	15	@ad<="19991018" and counter and ((PWM or (pulse adj width adj modulat\$3)) with resistor with capacitor) and (clock adj signal) and electromagnetic	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/04 11:08
S6	0	@ad<="19991018" and ((binary or digital) adj counter) and (up adj counter) and (down adj counter) and (bidirection adj counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/04 09:56
S7	497	@ad<="19991018" and ((binary or digital) adj counter) and (up adj counter) and (down adj counter) or (bidirection adj counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/04 09:57
S8	497	@ad<="19991018" and ((binary or digital) adj counter) and ((up adj counter) and (down adj counter)) or (bidirection adj counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/04 09:57
S9	490	@ad<="19991018" and ((binary or digital) adj counter) and ((up adj counter) and (down adj counter)) or (bidirection adj counter) and 375/238.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/04 09:58
S10	391	@ad<="19991018" and (binary adj counter) and ((up adj counter) and (down adj counter)) or (bidirection adj counter) and 375/238.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/04 09:59
S11	391	@ad<="19991018" and (binary adj counter) and ((up adj counter) and (down adj counter)) or (unidirection adj counter) and 375/238.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/04 09:59
S12	1	@ad<="19991018" and (binary adj counter) and (((up adj counter) and (down adj counter)) or (unidirection adj counter)) and 375/238.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/04 09:59
S13	1	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) with (timing adj resistor) with (timing adj capacitor)) and electromagnetic	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/04 11:11

S14	19	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) and (timing adj resistor) and (timing adj capacitor)) and electromagnetic	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/29 12:39
S15	33	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) and (timing adj resistor) and (timing adj capacitor)) and (clock adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/05 12:56
S16	0	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) and (timing adj resistor) and (timing adj capacitor)) and (clock adj signal) and (frequency adj varying)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/29 12:40
S17	4	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) and (timing adj resistor) and (timing adj capacitor)) and (clock adj signal) and (vary\$3 adj frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/29 12:51
S18	1	@ad<="19991018" and (((PWM or (pulse adj width adj modulat\$3)) same ((vary\$3 or different) adj frequency)) and (timing adj resistor) and (timing adj capacitor)) and (clock adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/29 13:08
S19	2560	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) same (high adj frequency))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/29 13:10
S20	1648	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) with (high adj frequency))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/29 13:11
S21	92	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) with (high adj frequency)) and EMI	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/29 13:11
S22	1	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) with (high adj frequency)) and EMI and 375/238.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/29 13:14
S23	92	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) with (high adj frequency)) and EMI	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/29 13:14

S24	0	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) with (high adj frequency)) and EMI and (clock adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/29 13:14
S25	7	@ad<="19991018" and ((PWM or (pulse adj width adj modulat\$3)) with (high adj frequency)) and EMI and (clock adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2003/11/29 13:15